## Notice of References Cited Application/Control No. 10/721,448 Examiner Eric B. Chen Applicant(s)/Patent Under Reexamination BRASK ET AL. Art Unit Page 1 of 1

## U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,850,683	02-2005	Lee et al.	385/129
	В	US-4,695,122	09-1987	Ishida et al.	385/130
	С	US-6,224,713	05-2001	Hembree et al.	156/345.11
	D	US-5,976,767	11-1999	Li, Li	430/313
	Ε	US-2004/0240822	12-2004	Patel et al.	385/130
	F	US-3,999,835	12-1976	Newns et al.	385/142
	G	US-5,466,389	11-1995	Ilardi et al.	510/175
	Н	US-4,817,652	04-1989	Liu et al.	134/102.1
	ı	US-5,841,931	11-1998	Foresi et al.	385/131
	J	US-6,890,450	05-2005	Naydenkov et al.	216/24
	К	US-			
	L	US-			
	М	US-			

## FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р	•				
	σ			•		
	R					
	S					
	Т					

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	ט	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, pp. 519, 529-30, 534, 551.
	٧	
	8	
	x	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.